



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 6448**
Takashige HIRATSUKA : Attorney Docket No. 2004_0503A
Serial No. 10/814,182 : Group Art Unit 2627
Filed April 1, 2004 : Examiner A. M. Psitos
TRACKING ERROR DETECTION APPARATUS : **Mail Stop: AMENDMENT**

SUBMISSION OF REPLACEMENT DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 111-9979

Sir:

Submitted herewith is 1 sheet of formal (A4) drawing (Fig. 6). As described in the Amendment filed concurrently herewith, a replacement Fig. 6 is filed herewith to amend the "valid edge" beneath the difference $\Delta 1$ to correctly indicate that the binary signal B falls before the binary signal A as is evident from the zero cross points of phase comparison inputs A and B which result in the difference $\Delta 1$.

Respectfully submitted,

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